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May 28, 1999

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TITLE: FORMATION OF DISPLAY TRANSISTOR ARRAY PANEL

PUBN-DATE: May 28, 1999

INVENTOR-INFORMATION:

NAME

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SHIMIZU, MASABUMI

ASSIGNEE-INFORMATION:

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SHARP CORP

APPL-NO: JP09310299

APPL-DATE: November 12, 1997

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ABSTRACT:

PROBLEM TO BE SOLVED: To sharply reduce a manufacturing cost.

SOLUTION: Plural TFT elements 43 are formed on a 1st substrate consisting of a Si substrate at pitches dx/m, dy/n with respective element separation grooves 44 intervened. The dx and dy are array pitches of pixels and each of (m) and (n) is a natural number of

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